

Day : Saturday
Date: 1/12/2008

Time: 14:54:23

 PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = ANDRE GENDARME

First Name = CHRISTOPHE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10736755	Not Issued	71	12/17/2003	Integrated modem circuit operating at 1 Mbit/s or more	ANDRE GENDARME, CHRISTOPHE BERNARD
10801676	Not Issued	30	03/17/2004	Matrix architecture for DSL applications	ANDRE GENDARME, CHRISTOPHE BERNARD

Inventor Search Completed: No Records to Display.

Search Another: Inventor**Last Name**

ANDRE GENDARME

First Name

CHRISTOPHE

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

Day : Saturday
Date: 1/12/2008

Time: 14:54:48

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = MARICHAL

First Name = ROEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10729023	7251720	150	12/08/2003	FLEXIBLE DIGITAL SIGNAL PROCESSOR	MARICHAL, ROEL LUC RITA
10801676	Not Issued	30	03/17/2004	Matrix architecture for DSL applications	MARICHAL, ROEL LUC RITA

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name
	<input type="text" value="MARICHAL"/>	<input type="text" value="ROEL"/>
		<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

Day : Saturday
Date: 1/12/2008

Time: 14:55:10

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = AERTS

First Name = BERT RENE ANNA MARIA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10801676	Not Issued	30	03/17/2004	Matrix architecture for DSL applications	AERTS, BERT RENE ANNA MARIA

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name
	<input type="text" value="AERTS"/>	<input type="text" value="BERT RENE ANNA MARIA"/>
		<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

Day : Saturday
Date: 1/12/2008

Time: 14:55:40

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = VAN HOOGENBEMT

First Name = STEFAAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08260771	5471511	150	06/16/1994	DIGITAL PHASE-LOCKED LOOP ARRANGEMENT FOR USE IN A DESYNCHRONIZER	VAN HOOGENBEMT, STEFAAN M.A.
10729023	7251720	150	12/08/2003	FLEXIBLE DIGITAL SIGNAL PROCESSOR	VAN HOOGENBEMT, STEFAAN MARGRIET ALBERT
10736755	Not Issued	71	12/17/2003	Integrated modem circuit operating at 1 Mbit/s or more	VAN HOOGENBEMT, STEFAAN MARGRIET ALBERT
10801676	Not Issued	30	03/17/2004	Matrix architecture for DSL applications	VAN HOOGENBEMT, STEFAAN MARGRIET ALBERT
09027667	6111899	150	02/23/1998	INTERFACING DEVICE TO REPLACE M SETS OF BITS OUT OF N SETS OF BITS, CONTROL UNIT AND LOGICAL CELL	VAN HOOGENBEMT, STEFAAN MARGRIET ALBERT

Inventor Search Completed: No Records to Display.

Search Another: Inventor**Last Name**
First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)